





Rev 1:

Added 74HC125 buffer chip for SCK, RESET, and WD_LATCH lines.
Added GND and VCC test points.

Rev 2:

Connected pin 2 of PPDO (out) to Vcc.

Rev 3:

Added edge detection circuit to RESET, BS0, 5VRST, and WD_LATCH signals.
Added Vcc selection to DS1834.
Modified circuit to use DS1834 rather than DS1834D.

Rev 4:

Re-laid out board for better power control; increased SSR contact trace sizes.

Plantation Productions, Inc.

TITLE: *ssr16*

Design by:

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REV:

Date: 4/13/17 4:54 PM

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